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Docket No.: R2180.0159/P159  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Kazunari Kimino

Application No.: 10/609,634

Confirmation No.: 4954

Filed: July 1, 2003

Art Unit: 2814

For: APPARATUS AND METHOD FOR  
MANUFACTURING SEMICONDUCTOR  
DEVICE

Examiner: Dilinh P. Nguyen

**RESPONSE TO RESTRICTION REQUIREMENT**

U.S. Patent and Trademark Office  
220 20th Street S.  
Customer Window  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, VA 22202

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed December 10, 2004, Applicant hereby elects Group III (claims 1-11 and 23-40) for continued examination, without traverse.

Applicant solicits issuance of claims 1-19.

Dated: January 10, 2005

Respectfully submitted,

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